

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
17 July 2003 (17.07.2003)

PCT

(10) International Publication Number
WO 03/058591 A1

(51) International Patent Classification⁷: G09G 3/28

(21) International Application Number: PCT/IB02/05598

(22) International Filing Date:
23 December 2002 (23.12.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
102 00 827.2 11 January 2002 (11.01.2002) DE

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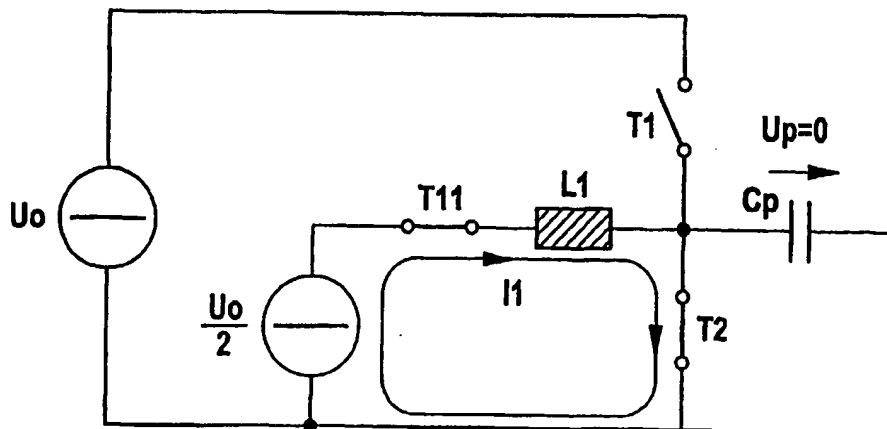
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(81) Designated States (national): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE,
SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ,
VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK,
TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
GW, ML, MR, NE, SN, TD, TG).

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(54) Title: METHOD OF CONTROLLING A CIRCUIT ARRANGEMENT FOR THE AC POWER SUPPLY OF A PLASMA
DISPLAY PANEL



(57) Abstract: A method of controlling a circuit arrangement for an AC voltage supply of a plasma display panel, the circuit arrangement comprising at least a transistor bridge constituted by the bridge transistors (T1, T2, T3, T4), an input voltage (U0), a capacitor (Cp) of the plasma cell and a charging circuit comprising an auxiliary voltage (Uh), a first auxiliary transistor (T11) and a first coil (L1) and at the beginning of the charging operation the first auxiliary transistor (T11) is turned on, characterized in that once the first auxiliary transistor (T11) has been turned on, the second bridge transistor (T2) of the half bridge continues to be turned on for a delay time tv and is turned off after the delay time tv has elapsed.

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